PCT/EP2003/006368

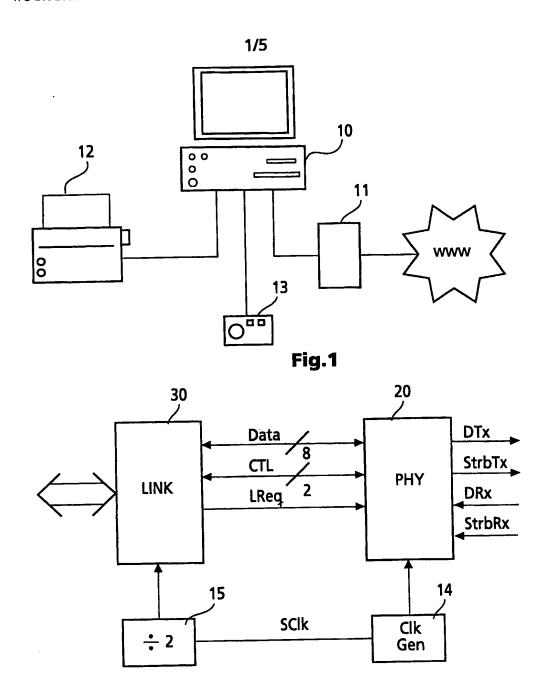


Fig.2

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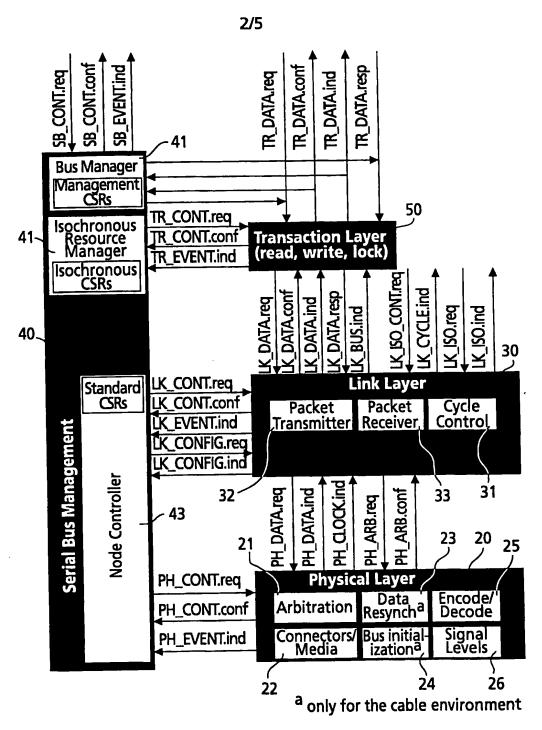
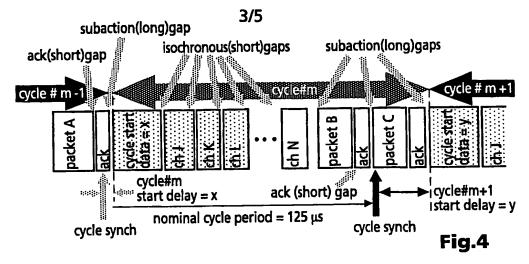
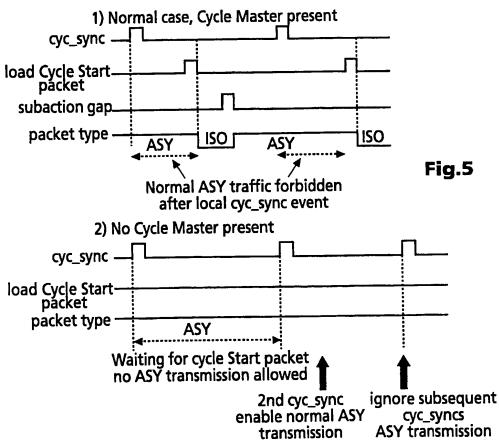


Fig.3

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No Cycle Master state always enabled

Fig.6

detected

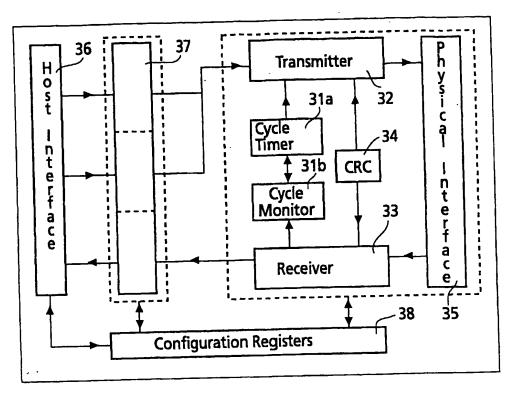


Fig.7

transı	mitted first	<u> </u>			· · · · · · · · · · · · · · · · · · ·						1:
10	phy_ID	OL	gap_cnt	sp	del	c	pwr	p0	рт	pΖ	1 m
logical inverse of first quadlet											
self_ID packet # 0								transmitted last			

Fig.8

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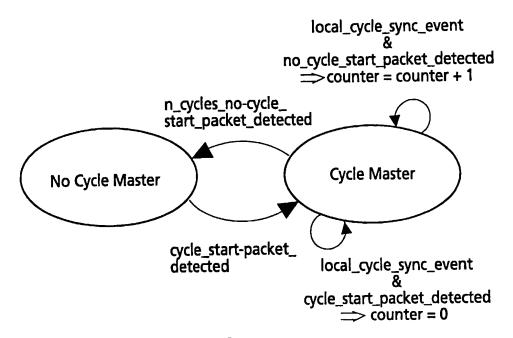


Fig.9

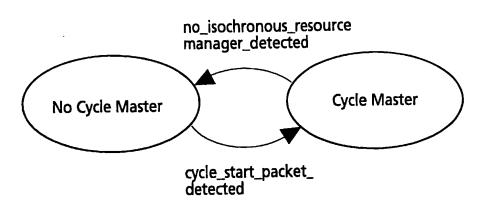


Fig.10